## In The Claims

## Claim 1 has been amended as follows:

- 1. (Amended) A metal oxide semiconductor (MOS) device
  comprising:
- a semi-conducting substrate having source and drain regions;
- a gate dielectric layer of less than 100  $\hbox{\normalfont\AA}$  thickness on said semi-conducting substrate; and
- a gate formed of a metal selected from the group consisting of Re, Rh, Tr) and Ru on top of said gate dielectric layer.

## Claim 10 has been amended as follows:

10. (Amended) A field effect transistor (FET) comprising:

- a semi-conducting substrate having at least one source and one drain region;
- a gate dielectric layer of less than 100  $\hbox{\normalfont\AA}$  thickness on the semi-conducting substrate; and
- a gate formed of a metal selected from the group consisting of Re, Rh, Ir and Ru on top of the gate dielectric layer.